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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Praveen K. Samudrala et al.

Serial No.: 10/708,268

Filing Date: 2/20/2004

For: Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices

Art Unit: 2819

Examiner: Daniel D. Chang

Confirmation No.: 2267

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the nonfinal Examiner's Action mailed on 1/11/2005, having a period of response that expired 4/11/2005 and an extended period of response set to expire 5/11/2005, the above-identified patent application is amended a first time as follows. Applicants have elected to present the amendment using the revised amendment format set forth in the waiver of 37 CFR 1.121.

AMENDMENT A
(37 C.F.R. § 1.121)